## UNITED STATES PATENT & TRADEMARK OFFICE Washington, D.C. 20231

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Wu et al.

Serial No.: 10/820,320

Filed: April 8, 2004

Title: Whole Chip ESD Protection

Group Art Unit: Unassigned

Examiner: Unassigned

Confirmation No. 8523

TKHR Dkt: 252016-1921 TSMC Dkt: TS01-1357B

**Certificate of Mailing** 

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to: Mail Stop Petitions; Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on June 9, 2005.

Signature - Hui Chin Barnhill

Mail Stop Petitions Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## RESPONSE TO NOTICE OF INCOMPLETE NONPROVISIONAL APPLICATION AND PETITION TO ACCORD THIS DIVISIONAL APPLICATION A FILING DATE OF APRIL 8, 2004

Sir:

On May 13, 2005 (over one year after the Applicant filed the above-referenced patent application), the Patent Office mailed a Notice of Incomplete Nonprovisional Application in the above-referenced patent application. The Undersigned respectfully submits that the Notice was mailed in error and as such should be withdrawn, and the original filing date of April 8, 2004 be granted.

06/15/2005 SSITHIBI 00000161 200778 1082032 01 FC:1462 400.00 DA